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Analysis and Estimation of the Maximum Switch Current during Battery System Reconfiguration

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Abstract—Batteries are interconnected in series and/or parallel to meet wide-range power or energy demands in various industrial applications. To pursue the benefits of multiple connection structures in one system, reconfigurable battery systems (RBSs) have recently emerged for safe and efficient operation, extended energy storage and delivery, etc. Switches are the essential elements to enable the battery system reconfiguration, but selecting appropriate switches for RBS designs has not been systematically investigated. To bridge this gap, analytical expressions are derived in this paper to estimate the maximum switch current and its upper limit to facilitate the selection of RBS switches. An RBS prototype based on H-bridges is set up and experimental results verify the effectiveness and advantage of the proposed estimation method. These analytical expressions, relying only on resistances of batteries and switches, are readily applicable to practical RBS design and much more efficient than conducting numerous circuit experiments, simulation tests, or circuit analyses, especially for large-scale systems. Moreover, the analysis framework and estimation method proposed for series-parallel mutual conversion can be adaptively extended to other complex system reconfigurations to facilitate various RBS designs.

Index Terms—Battery system, estimation, parallel connection, reconfiguration, switch current analysis.

I. INTRODUCTION

Battery energy storage and delivery have been playing an important role in a variety of industrial applications, such as transportation electrification and integration of wind and solar energy to power grids. The functionality and performance of a multi-battery system depend not only on the batteries applied but also on the way they are interconnected, i.e., the system configuration. As compared to the research and industrial efforts devoted to developing various high-performance batteries within a fixed configuration, far less attention has been paid to investigating the influence of dynamic system configurations. However, it has been revealed that appropriately manipulating

the system configuration can bring about a number of potential benefits [1], in terms of, e.g., enhanced fault tolerance [2], improved charge balance [3], [4], extended energy delivery [5], programmable terminal voltages [6], as well as hybrid use of batteries of different ages, types, and even chemistries [7]. In view of this, reconfigurable battery systems (RBSs) have been recognized as a promising direction to advanced battery management and control [1].

To set up an RBS, switches are introduced and connected between battery cells/modules/packs. As a result, unlike a conventional battery system of a fixed configuration, an RBS can operate in different interconvertible connection structures by controlling the states of switches. Various RBS topology designs and hardware implementations have been extensively reviewed in [1], [8], [9]. For instance, as shown in Fig. 2 and Table 1 in [1], a total of 11 emerging RBS topology designs have been compared and analyzed. These RBSs were designed for enhanced fault tolerance, customized terminal voltage range, charge and temperature balance, extended energy delivery, hybrid application of heterogeneous batteries, etc. To pursue these benefits, RBS management principles as well as control and optimization algorithms have been comprehensively discussed in [1]. On the other hand, to date, several challenges still need to be addressed in the RBS hardware design, system modeling, algorithm development, switch operations, scalability, etc.

During the hardware design of RBSs, to implement various system configurations with guaranteed safety under any encountered condition, RBS components should be appropriately selected based on their operating limits. In addition to choosing proper batteries and wires as required in all battery system designs, particular attention needs to be paid to the switch selection in the RBS hardware design. Due to the heating effect caused by resistive losses in internal components of switches, the continuous and various pulsed switch currents are bounded by certain limits specified by manufacturers. For instance, the switch in [10] can conduct currents up to 5.2 A for continuous operation and up to 21 A for a 160-microsecond pulse. When an RBS is reconfigured, e.g., from series to parallel, switch currents can dramatically change and potentially render substantial spikes (as will be illustrated in Section IV). While such current spikes typically fade very quickly, an inappropriately selected switch might suffer from overcurrent for certain pulse duration, causing progressive damage to the switch often with little noticeable temperature rise before failure [11]. However, the maximum RBS switch

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currents, especially during the system reconfiguration, have not been systematically studied in the literature.

To bridge this research gap, this paper will focus on analyzing and estimating the maximum switch currents for the switch selection in a typical RBS. The evolution of switch currents during battery system reconfiguration depends on the battery terminal voltages as well as the original and new system connection structures. Given the requested system reconfiguration, e.g., from series to parallel, a variety of possible battery voltages as well as their permutations need to be considered for quantifying the maximum switch current magnitudes. Conducting an exhaustive search among all possible cases by circuit experiments or model-based simulations would be too inefficient in energy and time, especially for large-scale RBSs. Moreover, instead of investigating only one fixed configuration in conventional battery systems, all desired configurations of the RBS along with their mutual conversions need to be comprehensively assessed. The maximum switch currents under different RBS configurations need to be well coordinated to ensure safe operation.

To address these challenges, particularly during the system reconfiguration, analytical expressions for directly evaluating the maximum switch current as well as its upper limit are first derived. As the major contribution of this paper, the proposed estimation equations are readily applicable, and, hence, much more energy- and time-efficient than exhaustively testing various battery voltage permutations by experiments or simulations. The estimated maximum switch currents and upper limits provide a ground for selecting appropriate switches for RBSs as well as checking the feasibility of the requested reconfiguration. Moreover, while the analysis and estimation methods are proposed for the reconfiguration between basic series and parallel connections, they are potentially extendable to more complicated reconfiguration operations which, in essence, are still composed of these two basic connection patterns, so as to cater for various RBS designs.

In the remainder of this paper, the following investigations will be subsequently conducted, significantly extending our preliminary work in [12]. Specifically, a typical RBS circuit design, allowing flexible conversion between series and parallel battery connections, will be mathematically modeled in Section II. Based on this model along with reasonable assumptions, the maximum switch currents under various system configurations and operating conditions will be analyzed and estimated, and their upper limits will then be derived in Section III. The effectiveness of the proposed estimation method will be verified by circuit experiments on an RBS prototype designed based on H-bridge converters in Section IV. Finally, conclusions and future work will be presented in Section V.

II. SYSTEM MODELING OF RECONFIGURATION CIRCUIT DESIGN

A. Circuit design for flexible series and parallel connections

In order to realize different configurations in a battery system, switches are commonly inserted between battery

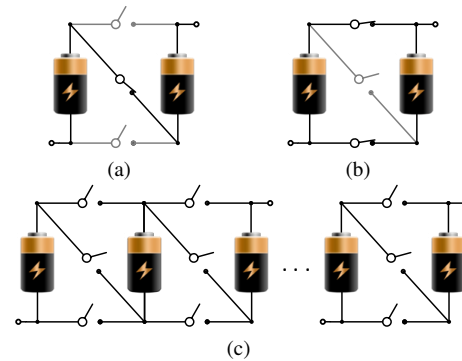


Fig. 1. Arrangement and working principle of an RBS allowing flexible conversion between series and parallel battery connections. (a) Series connection. (b) Parallel connection. (c) RBS design for multiple cells.

cells/modules/packs to activate or terminate their electrical connections. Depending on the number of switches around each battery and the battery-switch connection pattern, various RBS designs have been proposed recently and they were reviewed in [1], [8], and [9]. In all these RBS designs, batteries are locally connected in either series or parallel. In view of this, a typical RBS design enabling the mutual conversion between these two basic connection patterns is studied in this section.

Consider interconvertible series and parallel connections of only two battery cells at first. Clearly, the series and parallel connections of two cells should be exclusive to avoid any short circuit. Thus, when reconfiguring two parallel-connected cells to series connection, two switches, referred to as *parallel switches*, are needed to deactivate the original parallel connection (See Fig. 1 (a)). Then, to convert the series configuration back to a parallel connection, another switch, referred to as the *series switch*, needs to be inserted to cut off the series conduction path (See Fig. 1 (b)). Therefore, at least three switches are required to enable the flexible conversion between series and parallel connections of any two adjacent battery cells. Finally, the two-cell system can be scaled up to incorporate more cells, as shown in Fig. 1 (c).

Through flexible series-parallel reconfiguration in such an RBS design, the system can provide a wide range of terminal voltage [1], [13]–[15], which can facilitate the operation of various EV components with different voltage levels and waveforms. For some fast transient scenarios requiring high switching frequency, multiple battery cells/modules need to be reconfigured as simultaneously as possible [14]. In addition, any fully charged/discharged or faulty cell in a series-connected battery string can be individually isolated on the fly to achieve continuous and robust system operation. This reconfigurable topology was also applied to supercapacitors in [16] to improve the cycling efficiency and capacity utilization.

B. Equivalent circuit diagrams

During the hardware design, RBS components should be selected to allow for various connection structures and working conditions. To perform various tests, a well-established circuit model would be an efficient tool. Not only all components, such as battery cells, switches, and wires, but also their

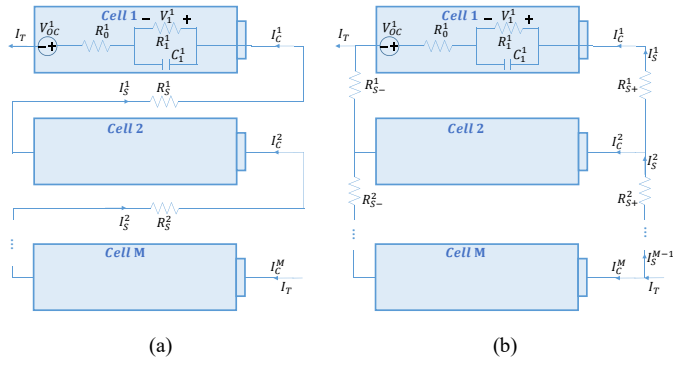


Fig. 2. Equivalent circuit diagrams of two basic configurations of the RBS in Fig. 1. (a) Series connection. (b) Parallel connection.

possible connection structures need to be modeled. Through different switch operations in the RBS in Fig. 1, M battery cells can get connected in one of the two basic configurations, either series or parallel, and their equivalent circuit diagrams are illustrated in Fig. 2 (a) and (b), respectively.

To model battery cells, equivalent circuit models (ECMs) are deployed since they can directly describe the electrical characteristics of batteries at an acceptable computational cost. It can be seen from Fig. 2 that a typical battery cell ECM is composed of a voltage source indicating its open circuit voltage (OCV) denoted by V_{OC}^m , $m = 1, \dots, M$, a resistor characterizing its internal resistance R_0^m , and $N \geq 1$ parallel resistor-capacitor (RC) pairs to describe the dynamic battery behavior such as the diffusion effect. The n -th RC pair in the m -th cell's ECM consists of a resistor R_n^m connected in parallel with a capacitor C_n^m , and its voltage is denoted by V_n^m , $n = 1, \dots, N$. For clear illustration, $N = 1$ is set for all cells in Fig. 2.

The switch modeling depends on the type of switches to be applied. MOSFET switches outperform mechanical ones in terms of much lower power demand for actuation and better synchronization performance for parallel operation [17]. Thus, MOSFET switches are employed here and modeled by its on-resistance, defined by the total resistance on the conductive path when the switch is turned on [18]–[20]. Specifically, as shown in Fig. 2, the series switch's resistance is denoted by R_S^m , and the resistances of parallel switches connecting the positive and negative battery terminals are denoted by R_{S+}^m and R_{S-}^m , respectively. In addition, since the resistances of connection wires or contacts can be easily incorporated into the resistances of nearby series-connected battery cells or switches, they are not explicitly shown in Fig. 2.

C. Mathematical modeling

For the series battery connection in Fig. 2 (a), clearly, all battery and switch currents are always equal to the RBS's total current I_T . Thus, batteries and switches should be capable of conducting the maximum charging and discharging currents of the RBS. When the system is reconfigured to the parallel connection as shown in Fig. 2 (b), however, the maximum switch currents are not so straightforward anymore due to the dynamic distribution of the total current to all parallel cells. Then, a mathematical model is formulated for analyzing the switch current evolution during the series-to-parallel reconfig-

uration and the following parallel operation. To simplify the notation, all time indices are omitted in this section.

During the RBS operation, the time-varying battery OCVs and RC pair voltages are selected as state variables to compose the state vector $x = [V_{OC}^1, V_1^1, \dots, V_N^1, \dots, V_{OC}^M, V_1^M, \dots, V_N^M]^T$, $x \in \mathbb{R}^{M(N+1)}$, such that

$$\dot{V}_{OC}^m = \frac{dV_{OC}^m}{dz_m} \dot{z}_m = k_{VS} \frac{\dot{Q}_m}{Q_C} = \frac{k_{VS}}{Q_C} I_C^m, \quad (1)$$

$$\dot{V}_n^m = \frac{1}{C_n^m} \left(I_C^m - \frac{V_n^m}{R_n^m} \right) = \frac{-1}{R_n^m C_n^m} V_n^m + \frac{1}{C_n^m} I_C^m, \quad (2)$$

$n = 1, \dots, N.$

In (1), z_m is the m -th cell's state of charge (SoC), defined by the ratio of its present amount of charge Q_m and its charge capacity, and I_C^m denotes the m -th cell's current, positive for charging. Assume all cells have the same charge capacity Q_C and share the same OCV-SoC curve. Then, $k_{VS}(z_m)$ denotes the slope of the OCV-SoC curve, dependent on the real-time SoC z_m or OCV of each cell, and, hence, the system state x .

Denote the cell current vector by $I_C = [I_C^1, \dots, I_C^M]^T$, then the state-space representation of battery cells can be generalized from (1) and (2) as

$$\dot{x} = A_C x + B_C I_C. \quad (3)$$

The system evolution represented by (3) is driven by all cell currents in I_C , which cannot be individually assigned during the parallel operation due to the dynamic parallel current distribution. Alternatively, the total current of the RBS, I_T , can be directly controlled. Thus, the input I_C in (3) will be expressed in terms of the total system current I_T as follows.

Applying Kirchhoff's voltage law (KVL) to the loop composed of the m -th and $(m+1)$ -th cells and two parallel switches in Fig. 2 (b), $m = 1, 2, \dots, M-1$, we have

$$\begin{aligned} -V_{OC}^m - I_C^m R_0^m - \sum_{n=1}^N V_n^m - I_S^m R_{S+}^m + \sum_{n=1}^N V_n^{m+1} \\ + I_C^{m+1} R_0^{m+1} + V_{OC}^{m+1} + (I_T - I_S^m) R_{S-}^m = 0, \end{aligned} \quad (4)$$

where I_S^m denotes the current through the parallel switch connecting the positive terminals of the m -th and $(m+1)$ -th cells. Then, applying Kirchhoff's current law (KCL) to the positive terminals of M cells, we have

$$I_S^m = \sum_{k=1}^m I_C^k, \quad m = 1, 2, \dots, M-1, \quad (5)$$

$$I_T = I_S^{M-1} + I_C^M = \sum_{m=1}^M I_C^m. \quad (6)$$

Combining and reorganizing (6) and the $M-1$ equations in (4) with I_S^m substituted by (5) leads to

$$E x + F I_C = G I_T \Rightarrow I_C = -F^{-1} E x + F^{-1} G I_T. \quad (7)$$

Then, based on (3) and (7), the state-space representation for the RBS parallel operation driven by I_T is

$$\dot{x} = (A_C - B_C F^{-1} E) x + B_C F^{-1} G I_T \triangleq A x + B I_T. \quad (8)$$

Finally, according to (5), the switch current vector, denoted by $I_S = [I_S^1, \dots, I_S^{M-1}]^T$, can be represented by the cell current vector I_C through a transition matrix, denoted by T_{SC} , and based on (7),

$$\begin{aligned} I_S &= T_{SC} I_C \\ &= -T_{SC} F^{-1} E x + T_{SC} F^{-1} G I_T \triangleq C_S x + D_S I_T. \end{aligned} \quad (9)$$

The involved matrices A_C , B_C , E , F , G , and T_{SC} in (3), (7), (8), and (9) can be derived based on (1), (2), (4), (5), and (6), and their entries will not be presented here for brevity. Note that, B_C , A , and B in (8) are not constant since B_C has entries, i.e., $\frac{k_V S}{Q_C}$, dependent on the evolution of cell SoCs or OCVs. Thus, (8) represents a time-varying system.

This system model (8) along with the output equation (9) can be used to simulate and analyze the switch current evolution during the RBS series-to-parallel operation. However, since a lot of battery states and input currents are possible, it is too inefficient to test all possible cases for identifying the maximum switch current. To accomplish this, direct analytical expressions for the maximum RBS switch currents as well as their upper limits will be derived.

III. ANALYSIS AND ESTIMATION OF THE MAXIMUM SWITCH CURRENT

As discussed previously, once the system is reconfigured to the series connection, the maximum series switch current can be directly determined by the maximum external current of the RBS. Thus, the maximum switch current analysis will be mainly focused on the series-to-parallel reconfiguration and the following parallel operation based on the developed model in Section II. Consider new cells and switches sharing identical parameter values, i.e., for $\forall m$ in Fig. 2 (b),

$$R_0^m = R_C, \quad R_n^m = R_n, \quad C_n^m = C_n, \quad R_{S+}^m = R_{S-}^m = R_S. \quad (10)$$

A. Current analysis for the series-to-parallel reconfiguration

Consider battery cells first operating in series and then being reconfigured to parallel connection. During the series operation, since all cells always share the same charging or discharging current, the SoC difference between battery cells remains unchanged, and, accordingly, the cell OCVs cannot get balanced. Other entries in the state vector, i.e., RC pair voltages, however, can be shown to gradually get equalized during the series operation as follows.

Based on (2) and (10), for each RC pair in each cell,

$$V_n(t) = e^{-\frac{t}{R_n C_n}} V_n(0) + \int_0^t e^{-\frac{t-\tau}{R_n C_n}} \frac{I_T(\tau)}{C_n} d\tau, \quad (11)$$

in which the first term always decays over time and will approach zero when the series operation time is much longer than the time constant $\tau_n = R_n C_n$, typically only seconds. Thus, as the series operation goes on, each cell's $V_n(t)$ is gradually dominated by the second term in (11), which is always identical for all series-connected cells since they share the same total current $I_T(t)$ and parameter values of R_n , C_n based on (10). In other words, after a sufficiently long period

of series operation, all battery cells will have identical RC pair voltages, and, hence, the cell state imbalance will only depend on cell OCVs.

Once the system is reconfigured to parallel operation, the switch current I_S in (9) can be divided into the zero-input and zero-state terms, i.e.,

$$I_{Szi} \triangleq C_S x, \quad (12)$$

$$I_{Szs} \triangleq D_S I_T. \quad (13)$$

Then, the maximum switch current magnitude satisfies

$$\begin{aligned} \max_{m,t} |I_S^m(t)| &= \max_{m,t} |I_{Szi}^m(t) + I_{Szs}^m(t)| \\ &\leq \max_{m,t} (|I_{Szi}^m(t)| + |I_{Szs}^m(t)|) \\ &\leq \max_{m,t} |I_{Szi}^m(t)| + \max_{m,t} |I_{Szs}^m(t)|, \end{aligned} \quad (14)$$

where the superscript m indicates the m -th entry in vectors I_S , I_{Szi} , and I_{Szs} .

Based on (13), the zero-state term in (14) can be estimated by

$$\begin{aligned} \max_{m,t} |I_{Szs}^m(t)| &= \max_{m,t} |D_S^m I_T(t)| = \max_{m,t} (|D_S^m| |I_T(t)|) \\ &= \max_m |D_S^m| \max_t |I_T(t)| = \max_m |D_S^m| I_T^{ub} \triangleq |I_{Szs}^{max}|, \end{aligned} \quad (15)$$

where D_S^m denotes the m -th entry of the vector D_S , which is determined by all cell and switch resistances, and, thus, assumed constant over time. Then, $\max_m |D_S^m|$ can be easily identified based on the pre-calculated D_S . Besides, I_T^{ub} is the pre-specified upper bound for the magnitude of the total external current. Thus, $|I_{Szs}^{max}|$ can be directly calculated and the following analysis will be mainly concentrated on estimating the remaining zero-input term in (14).

Given zero external current during the parallel operation, if the cell terminal voltages are initially different, they will gradually get balanced with each other and cause decreasing balancing currents through switches over time. Then, the maximum switch current magnitude should be observed at the beginning of the parallel operation, and the corresponding time instant is denoted by t_p . To illustrate this, based on the developed model (8) with $I_T = 0$, the parallel operation of a 4-cell RBS is simulated in Fig. 3 with $t_p = 0$. As one can see, the maximum switch current's magnitude (marked by the blue solid curve) decreases over time. Thus, the zero-input term in (14) becomes

$$\max_{m,t} |I_{Szi}^m(t)| = \max_m |I_{Szi}^m(t_p)|. \quad (16)$$

To estimate this current magnitude, the initial balancing currents during the parallel operation will be intensively analyzed. For brevity, all the following variables and equations in this section will be presented at $t = t_p$ if not particularly specified.

B. Initial switch current analysis during the parallel operation without external charging/discharging current

As discussed at the beginning of Subsection III-A, during the series operation, RC pair voltages can gradually get equalized for all cells but the cell OCV imbalance cannot be alleviated. When the system is reconfigured from series

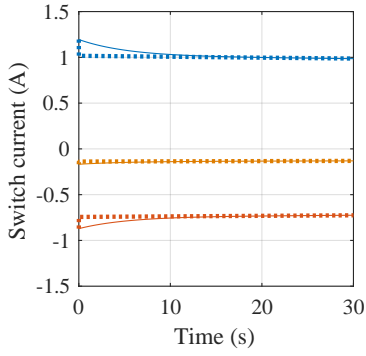


Fig. 3. The switch current evolution during the parallel operation of a 4-cell RBS based on the complete model represented by (8) (solid curves) and that based on the model without RC pairs (dot curves).

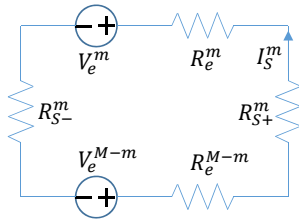


Fig. 4. Equivalent circuit of two two-terminal networks connected through parallel switches.

to parallel at $t = t_p$, both cell OCVs and RC pair voltages cannot change instantaneously according to (1) and (11). Thus, any cell OCV imbalance during the series operation will be reserved after the reconfiguration. However, the already equalized RC pair voltages will not contribute to the initial balancing current at $t = t_p$, and, thus, can be ignored for estimating the maximum initial switch current. This can be verified in Fig. 3. When RC pairs are removed from the circuit diagram in Fig. 2 (b), i.e., assuming $V_n^m(t) = 0$, the simulated switch currents (marked by dotted curves) start with the same initial currents as those simulated using the complete circuit model including RC pairs (marked by solid curves).

To analyze the switch current between any two adjacent cells at any time instant of the parallel operation without external charging/discharging current, the system can be viewed as two two-terminal networks connected through parallel switches, as illustrated in Fig. 4. The upper branch represents the network consisting of the first to the m -th parallel-connected cells in Fig. 2 (b), and the lower branch represents the network including all remaining parallel-connected cells. The two networks are connected through a pair of parallel switches. The Thevenin equivalent circuit parameters of the upper network can be directly calculated by the following proposition, and those of the lower network can be similarly obtained by counting the cells backward.

Proposition 1: At the beginning of the parallel operation in Fig. 2 (b), given a zero total external current, zero RC pair voltages, and identical parameter values as specified in (10), the network composed of the first to the m -th parallel-connected cells can be represented by an equivalent voltage source with voltage V_e^m connected in series with an equivalent resistor with resistance R_e^m . These parameters can be calculated by

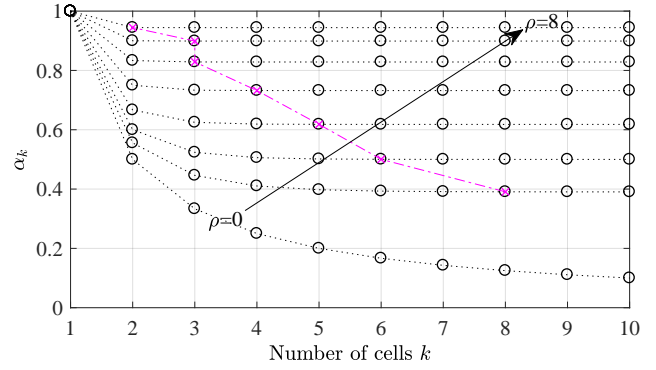


Fig. 5. Evolution of α_k with various switch-battery resistance ratios, $\rho \in \{0, \frac{1}{8}, \frac{1}{4}, \frac{1}{2}, 1, 2, 4, 8\}$, and the arrow indicates the ascending direction of ρ .

$$R_e^m = \alpha_m R_C, \quad (17)$$

$$V_e^m = \begin{cases} V_{OC}^1, & \text{if } m = 1, \\ \sum_{k=1}^{m-1} \left(\prod_{j=k+1}^m (1 - \alpha_j) \alpha_k V_{OC}^k \right) + \alpha_m V_{OC}^m, & \text{if } m > 1, \end{cases} \quad (18)$$

$$\text{where } \alpha_k = \begin{cases} 1, & \text{if } k = 1, \\ 1 - \frac{1}{\alpha_{k-1} + 2\rho + 1}, & \text{if } k = 2, \dots, m, \end{cases} \quad (19)$$

$$\rho = R_S / R_C. \quad (20)$$

1) *Analysis of the equivalent network resistance R_e^m :* It can be seen from (17) that R_e^m depends on the α_m which can be obtained according to the α_k sequence defined in (19) and relying only on the switch-battery resistance ratio ρ defined in (20). Given different resistance ratios, the α_k sequences are compared in Fig. 5. For each ρ , α_k decreases and eventually approaches convergence as the number of cells involved, i.e., k , increases. Given a larger ρ , α_k can converge to a higher value involving a smaller number of cells k (marked by magenta cross points). Here, a convergence tolerance $\epsilon_c = 10^{-3}$ is set. These observations can be mathematically proved and generalized in Lemma 1.

Lemma 1: Given $\rho \geq 0$ in the sequence of α_k defined in (19), as the number of involved cells k increases from 1, α_k monotonically decreases from 1 and finally converges to

$$\alpha_\infty(\rho) \triangleq \lim_{k \rightarrow +\infty} \alpha_k(\rho) = \sqrt{\rho^2 + 2\rho} - \rho. \quad (21)$$

Note that, the fast convergence property of α_k as seen in Fig. 5 can be applied to further simplify the calculation of the equivalent resistance R_e^m . Given $\rho > 0$ and the convergence tolerance ϵ_c , let $k_c(\rho, \epsilon_c)$ denote the smallest number of cells k such that $\alpha_k(\rho) - \alpha_\infty(\rho) \leq \epsilon_c$. Then, $\alpha_k(\rho) \approx \alpha_\infty(\rho)$, $\forall k \geq k_c$. Thus, according to (17), the equivalent resistance of any network including at least k_c cells can be approximated by a constant resistance, i.e.,

$$R_e^m \approx \alpha_\infty(\rho) R_C, \quad m \geq k_c(\rho, \epsilon_c), \quad \rho > 0. \quad (22)$$

2) *Analysis of the equivalent network voltage V_e^m :* Unlike the equivalent resistance only dependent on the sequence of α_k , according to (18), the equivalent voltage V_e^m is also influenced by the OCVs of all cells involved. The equivalent voltage V_e^m can be viewed as a weighted sum of all m OCVs

and it can be proved that all weights fall within $\in (0, 1]$ and sum up to one. Then,

$$V_e^m \in [\min_{1 \leq k \leq m} V_{OC}^k, \max_{1 \leq k \leq m} V_{OC}^k]. \quad (23)$$

Particularly, if all cells have the same initial OCV in a network, i.e., $V_{OC}^k = V_{OC}$, $k = 1, \dots, m$, then, according to (18), the equivalent voltage can be directly obtained as below regardless of the number of cells involved k and the resistance ratio ρ .

$$V_e^m (V_{OC}^k = V_{OC}, k = 1, \dots, m) = V_{OC}. \quad (24)$$

3) *Switch current expression based on the network model:* According to the equivalent circuit in Fig. 4, (10), (17), and (20), the initial magnitude of the m -th switch current is

$$|I_{Szi}^m| = \frac{|V_e^m - V_e^{M-m}|}{R_e^m + R_e^{M-m} + 2R_S} = \frac{|V_e^m - V_e^{M-m}|}{(\alpha_m + \alpha_{M-m} + 2\rho) R_C}. \quad (25)$$

Given the resistances of batteries and switches, the denominator in (25) can be evaluated based on (19) and (20), but the numerator still depends on the cell OCVs at the moment of the system reconfiguration according to (18). Thus, the switch currents under various cell OCVs are then analyzed.

C. Estimating the maximum switch current under various cell OCVs without total charging/discharging current

When reconfiguring the battery connection from series to parallel, battery OCVs could differ from each other and severe battery OCV imbalance could cause a series of progressive issues, such as biased current and thermal distributions, higher total power loss, reduced energy storage/delivery, and even safety hazards [21]. All battery OCVs and SoCs can be estimated using well-designed state estimators [22]–[24], and they do not change instantaneously during the system reconfiguration. Thus, a limit is commonly imposed on the range of all battery OCVs to restrict the unbalanced operation. Based on (23), the numerator of $|I_{Szi}^m|$ in (25)

$$|V_e^m - V_e^{M-m}| \leq V_{OC}^{ran} \leq V_{OC}^{rlim}, \quad (26)$$

where $V_{OC}^{ran} \triangleq \max_{1 \leq k \leq M} V_{OC}^k - \min_{1 \leq k \leq M} V_{OC}^k$ is the range of all M battery OCVs and V_{OC}^{rlim} is its upper limit. Particularly, according to (24), the equality in (26) can be achieved at some $m = m^*$, if $V_{OC}^1 = \dots = V_{OC}^{m^*}$, $V_{OC}^{m^*+1} = \dots = V_{OC}^M$, and $|V_{OC}^1 - V_{OC}^M| = V_{OC}^{rlim}$.

Moreover, given the resistances of batteries and switches, the switch-battery ratio ρ can be determined following (20), and the sequences of α_m and α_{M-m} can be calculated based on (19). Then, there exists some m^\dagger at which the sum of α_m and α_{M-m} , denoted by $S_\alpha^m(\rho, M)$, reaches its minimum, i.e.,

$$S_\alpha^{m^\dagger}(\rho, M) = \alpha_{m^\dagger} + \alpha_{M-m^\dagger} \triangleq \min_{1 \leq m \leq M-1} (\alpha_m + \alpha_{M-m}). \quad (27)$$

It can be shown that the α_k sequence defined by (19) is convex. Then, $m^\dagger = \lfloor \frac{M}{2} \rfloor$, and

$$S_\alpha^{m^\dagger}(\rho, M) = \alpha_{\lfloor \frac{M}{2} \rfloor} + \alpha_{M-\lfloor \frac{M}{2} \rfloor} = \alpha_{\lfloor \frac{M}{2} \rfloor} + \alpha_{\lceil \frac{M}{2} \rceil}. \quad (28)$$

Based on (19), (21), (22), (27), (28), and Lemma 1,

$$S_\alpha^{m^\dagger}(\rho, M) \geq 2 \left(\sqrt{\rho^2 + 2\rho} - \rho \right), \quad M > 1, \quad (29)$$

$$S_\alpha^{m^\dagger}(\rho, M) = \begin{cases} 2, & \text{if } M = 2, \\ 2 - \frac{1}{2(\rho+1)}, & \text{if } M = 3, \end{cases} \quad (30)$$

$$S_\alpha^{m^\dagger}(\rho, M) \approx 2 \left(\sqrt{\rho^2 + 2\rho} - \rho \right), \text{ if } M \geq 2k_c(\rho, \epsilon_c). \quad (31)$$

Accordingly, the denominator in (25) can be minimized to $(S_\alpha^{m^\dagger}(\rho, M) + 2\rho) R_C$.

In order to maximize the switch current in (25), the numerator is maximized and the denominator is minimized simultaneously, i.e., letting $m = m^* = m^\dagger$. Thus, based on the (25), (26), and (27), the maximum switch current magnitude in (16) under various cell OCVs is

$$\max_m |I_{Szi}^m(t_p)| \leq \frac{V_{OC}^{rlim}}{(S_\alpha^{m^\dagger}(\rho, M) + 2\rho) R_C} \triangleq |I_{Szi}^{max}|. \quad (32)$$

Then, based on (29), (30), (31), and (32), the maximum switch current magnitudes for different numbers of parallel-connected battery cells can be evaluated by

$$|I_{Szi}^{max}(M > 1)| \leq |I_{Szi}^{ulim}|, \quad (33)$$

$$|I_{Szi}^{ulim}| \triangleq \lim_{M \rightarrow +\infty} |I_{Szi}^{max}| = \frac{V_{OC}^{rlim}}{2\sqrt{\rho^2 + 2\rho} R_C} = \frac{V_{OC}^{rlim}}{2\sqrt{1 + \frac{2}{\rho}} R_S}, \quad (34)$$

$$|I_{Szi}^{max}(M = 2)| = \frac{V_{OC}^{rlim}}{2(\rho + 1) R_C}, \quad (35)$$

$$|I_{Szi}^{max}(M = 3)| = \frac{V_{OC}^{rlim}}{\left(2(\rho + 1) - \frac{1}{2(\rho+1)}\right) R_C}, \quad (36)$$

$$|I_{Szi}^{max}(M \geq 2k_c)| \approx |I_{Szi}^{ulim}|. \quad (37)$$

According to (33), the maximum parallel switch currents all share a common upper limit $|I_{Szi}^{ulim}|$, defined in (34) and independent of the number of parallel-connected battery cells M . For a small number of cells, the upper limits can be directly derived based on (28) and (32), e.g., (35) and (36). When the number of cells $M \geq 2k_c$, $S_\alpha^{m^\dagger}(\rho, M)$ approaches its minimum according to (31) and accordingly the maximum switch current approaches its upper limit, as given in (37).

D. Estimating the maximum switch current during reconfiguration considering the external charging/discharging current

Finally, taking into account both the voltage balancing between cells of various OCVs and the dynamic distribution of the wide-range external current to parallel cells, the maximum switch current magnitude $|I_S^{max}|$ and its upper limit $|I_S^{ulim}|$ can be derived based on (14), (15), (16), (32), (33), and (34),

$$\max_{m,t} |I_S^m(t)| \leq |I_S^{max}| \leq |I_S^{ulim}|, \quad (38)$$

$$\begin{aligned} |I_S^{max}| &\triangleq |I_{Szi}^{max}| + |I_{Szs}^{max}| \\ &= \frac{V_{OC}^{rlim}}{(S_\alpha^{m^\dagger}(\rho, M) + 2\rho) R_C} + \max_m |D_S^m| I_T^{ub}, \end{aligned} \quad (39)$$

$$\begin{aligned}
 |I_S^{ulim}| &\triangleq \lim_{M \rightarrow +\infty} |I_S^{max}| \\
 &= \frac{V_{OC}^{rlim}}{2\sqrt{\rho^2 + 2\rho R_C}} + \max_m |D_S^m| I_T^{ub} \\
 &= \frac{V_{OC}^{rlim}}{2\sqrt{1 + \frac{2}{\rho} R_S}} + \max_m |D_S^m| I_T^{ub}. \quad (40)
 \end{aligned}$$

To estimate the switch current upper limit, these analytical expressions, covering all possible battery states and total charging/discharging currents, are much more computationally efficient than performing a large number of circuit experiments and numerical simulations. While (40) yields a higher, and, hence, more conservative estimate than (39), it can quickly provide a reliable estimate without evaluating S_α^\dagger , especially when a large number of parallel-connected cells are involved.

It is worth noting that, only two parameter values are required when applying these analytical expressions, mainly the battery internal resistance and the switch on-resistance. For new cells and switches, these parameter values can be easily looked up from the datasheets. As battery cells age over time, the ratio ρ decreases since the battery internal resistance R_C generally increases [25] and the switch resistance R_S almost stays the same. According to the last line in (40), battery degradation contributes to a decreased upper limit of the maximum switch current. Then, the initially selected switches according to the current limit estimated based on the resistance of new battery cells, do not have to upgrade anymore due to the battery degradation. In addition, the battery cell internal resistance [26] The heterogeneous degradation of battery cells does not satisfy the assumption in (10) and will be separately investigated in our future work.

IV. VALIDATION OF THE ACCURACY AND EFFECTIVENESS

A. Circuit implementation based on H-bridge modules

To test the performance of the proposed estimation method, a laboratory prototype of the RBS design in Fig. 1 was built as shown in Fig. 6. In this prototype, LG 18650 2800 mAh battery cells [27] and H-bridges based on low-voltage MOSFETs [28] were used. In addition, automotive fuses rated for 20 A are attached for current protection.

To illustrate the operation of this RBS, the schematic of this prototype is shown in Fig. 7 (a). Specifically, the switches of each H-bridge are operated in pairs, and the switches of each half-bridge must be complementarily operated. As shown in Fig. 7 (b), both switches S_{k4} and $S_{(k+1)1}$, $1 \leq k < n$, are conducted in these H-bridge modules for parallel battery operation, while the remaining switches S_{k3} and $S_{(k+1)2}$ for series connection. Fig. 7 (c) depicts the logic of the gate signals to activate the parallel or the series connections of all batteries.

Based on the battery and MOSFET switch datasheets and experimental measurements at room temperature 20 °C, the following parameter values are extracted for estimating the maximum switch current. The total cell resistance is $R_C =$

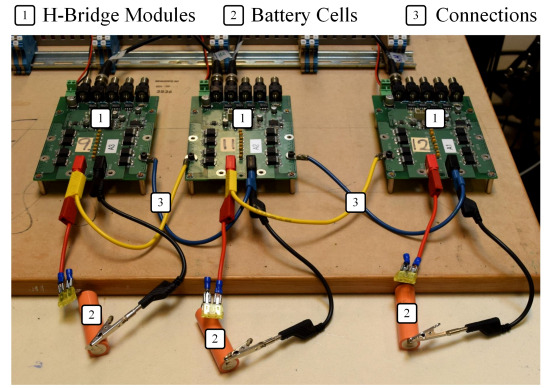


Fig. 6. Laboratory setup with three H-bridge modules.

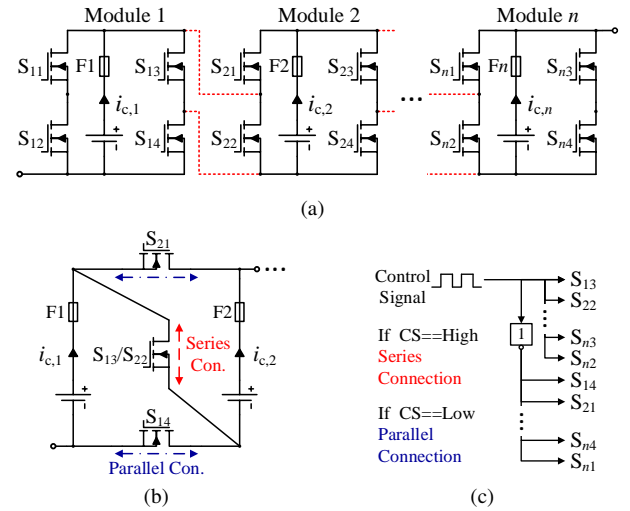


Fig. 7. (a) Schematic of the RBS implementation based on H-bridge modules. (b) Switches for series and parallel connections. (c) Gate signals for series and parallel connections.

71.5 mΩ, composed of the battery's internal resistance $R_0 = 62.1$ mΩ, the connection wire's resistance 6 mΩ, and the fuse's resistance 3.4 mΩ. Besides, the parallel switch resistance is $R_S = 5.3$ mΩ, consisting of the switch on-state resistance 1 mΩ and all the resistance along the conduction path on the printed circuit board. Then, based on (20), the switch-battery resistance ratio is $\rho = 0.0741$.

B. Circuit experiments for evaluating the performance of the proposed estimation method

As shown in Section III, the major breakthrough in this paper is deriving the analytical expressions for the maximum switch current and its upper limit during the series-to-parallel reconfiguration without external current. To evaluate the performance of the proposed method, circuit experiments were performed using the RBS prototype in Fig. 6. In all the following tests, battery cells, originally connected in series, were reconfigured to a parallel connection at $t_p = 0$ without external charging/discharging currents.

1) *Two-cell test*: Two cells with a large initial OCV difference of 1.1 V were first tested. As shown in Fig. 8, when these cells are reconfigured from series to parallel, the switch current (marked by blue dots) increases rapidly and reaches its peak 7.195 A. During this ascending period, while some

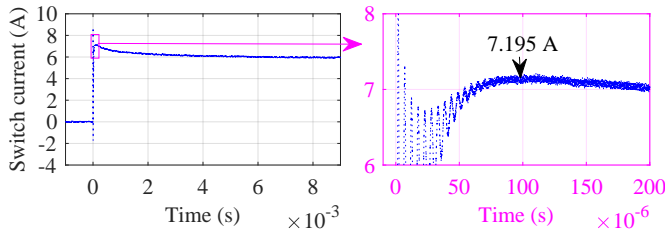


Fig. 8. Experimental switch currents during the series-to-parallel reconfiguration of a two-cell system with initial cell OCVs (4.17 V, 3.07 V).

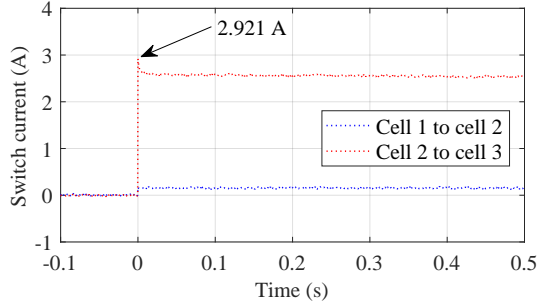


Fig. 9. Experimental switch currents during the series-to-parallel reconfiguration of a three-cell system with initial OCVs (3.969 V, 4.166 V, 3.717 V).

oscillations can be observed due to the resonance of the parasitic inductance and capacitance on associated elements, their magnitudes quickly diminish within microseconds and cannot cause significant damage to switches (e.g., the current limit is imposed to a 160-microsecond pulse in [10]). After passing the peak, the switch current decreases at a much slower pace than the previous ascending period. Thus, only the switch current peak will be analyzed.

Based on the extracted parameter values, the maximum switch current I_S^{max} can be estimated using (35) given the OCV range limit V_{OC}^{rlim} . To facilitate the comparison, set V_{OC}^{rlim} to the given OCV range, i.e., 1.1 V. Then, I_S^{max} is estimated to be 7.161 A, very close to the measurement 7.195 A with a percentage error of -0.47%, which verifies the estimation accuracy of the proposed method.

2) *Three-cell test*: To demonstrate the effectiveness and advantage of the proposed method, the experimental test was then extended to a three-cell system. It is aimed to estimate the maximum switch current among all possible cell OCV permutations within a specific cell OCV range limit to ensure the safe operation of switches. It would be too inefficient to exhaustively test all possible OCV permutations by experiments or model-based circuit analysis. Thus, one cell OCV permutation (3.969 V, 4.166 V, 3.717 V) was randomly selected and experimentally tested. The measured maximum switch current was 2.921 A as illustrated in Fig. 9.

The maximum switch current for this specific cell OCV permutation can also be evaluated through circuit analysis based on the circuit diagram in Fig. 2 (b) without RC pairs along with the parameter values identified in Section IV-A. The calculated maximum switch current, 2.970 A, is close to the above experimental measurement with a small percentage error of 1.66%, verifying the accuracy of these parameter values and the feasibility of circuit analysis-based current evaluation. Then, using a similar analysis procedure for each permutation

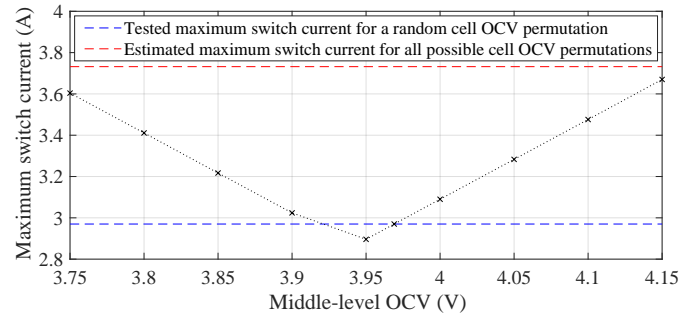


Fig. 10. Maximum switch currents during the series-to-parallel reconfiguration for the same highest cell OCV, 4.166 V, the same lowest cell OCV, 3.717 V, but various middle-level cell OCVs.

of these cell OCVs, we can obtain three different maximum switch currents, i.e., 2.970 A, 2.757 A, and 2.857 A, since any OCV permutation and its inverse lead to the same result. Thus, the experimentally tested cell OCV permutation happens to yield the maximum switch current of all six possible permutations of the three cell OCVs.

To facilitate the comparison of the proposed switch current estimation method with the above experimental test and circuit analysis, the cell OCV range limit in the proposed method is specified to the range of these three cell OCVs, i.e., $V_{OC}^{rlim} = 0.449$ V. However, various cell OCVs and permutations are possible during the system operation, including the above randomly picked cell OCV set tested by the experiment and circuit analysis. Even though the highest OCV, 4.166 V, and the lowest cell OCV, 3.717 V, are further fixed, the remaining middle-level cell OCV still varies in between. Then the maximum switch currents for various samples of middle-level cell OCVs are calculated through circuit analysis, as marked by black crosses in Fig. 10. It can be seen that some sampled maximum switch currents are above the tested one for the previous randomly selected cell OCV set (marked by the blue dash line), but all sampled currents fall below the estimated maximum switch current considering all possible cell OCV permutations, i.e., 3.732 A obtained by (36) and marked by the red dash line in Fig. 10. This verifies the effectiveness of the proposed method in terms of providing a reliable estimate of the maximum switch current for various possible operating scenarios. To obtain a reliable estimate, the proposed method outperforms time-consuming experimental tests or circuit analysis of various possible cell OCV permutations.

C. Estimating the maximum switch currents for larger systems

After experimentally verifying the accuracy and effectiveness of the proposed method, this method is deployed to assess the maximum switch currents in RBSs larger than the prototype system in Fig. 6. Given a limit of the battery cell OCV range, e.g., $V_{OC}^{rlim} = 0.3$ V, based on the battery and switch parameters extracted in Subsection IV-A, i.e., $R_C = 71.5$ m Ω and $R_S = 5.3$ m Ω , the maximum switch currents for RBSs composed of different numbers of cells are estimated using (19), (20), (28), and (32), and demonstrated in Fig. 11. The maximum switch current increases as more cells get involved, and approaches its upper limit estimated by (34)

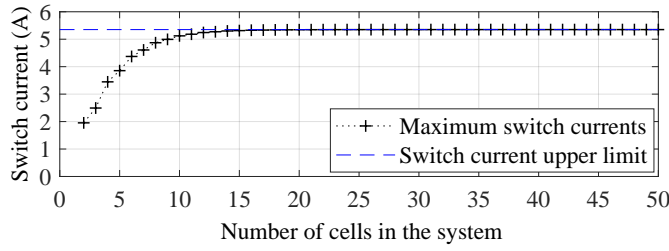


Fig. 11. The maximum switch currents and their upper limit as the number of cells increases in the system.

when the system has more than 12 cells. Thus, for large-scale systems, we only need to estimate the upper limit using (34) and it is reliable to directly view it as the system's maximum switch current regardless of the exact number of cells involved.

The maximum switch current is estimated mainly for the switch current rating during the initial transient period after the system reconfiguration. When it is applied to the switch current rating during the steady-state period, the switch's steady-state current is overrated to some extent, as shown in Fig. 8 and Fig. 9, but it leaves a margin above the actual operating limit, contributing to the reliable system operation.

V. CONCLUSIONS AND FUTURE WORK

Flexible system reconfiguration through switch operations provides a new option for battery system management and control. On the other hand, if switches are not properly selected in an RBS, some desired system configurations may become inefficient and even infeasible. This work first investigates the switch currents during battery system reconfiguration for selecting appropriate RBS switches. Specifically, the maximum switch current as well as its upper limit during the mutual conversion between series and parallel connections have been analyzed and estimated considering various possible battery voltages. An RBS prototype was set up and circuit experiments were carried out to demonstrate the accuracy and effectiveness of the proposed estimation method.

As compared to testing a large number of possible cell OCVs and permutations by circuit experiments or model-based numerical simulations, the proposed estimation method, presented in analytical expressions without massive search, is much more efficient in both energy and time. In addition, while focusing on the reconfiguration between basic series and parallel connections, this work provides a framework to further analyze more complex reconfigurations involving mixed series and parallel connections.

In future work, more temperature levels will be tested, and the influence of heterogeneous battery temperatures and ages on the maximum switch current will be further investigated. Moreover, following the analysis during the system reconfiguration, the switch current peaks throughout the operating cycle, mainly caused by the dynamic external charging or discharging currents, will be studied.

APPENDIX A NOMENCLATURE

All abbreviations and denotations are specified as follows.

RBS	Reconfigurable battery system
m	Battery cell index
M	The total number of cells in the battery system
ECM	Equivalent circuit model
OCV	Open circuit voltage
V_{OC}^m	Open circuit voltage of the m -th cell
R_0^m	Internal resistance of the m -th cell
N	The number of RC pairs in each battery cell ECM
R_n^m	Resistance of the resistor in the n -th RC pair in the m -th cell's ECM
C_n^m	Capacitance of the capacitor in the n -th RC pair in the m -th cell's ECM
V_n^m	Voltage of the n -th RC pair in the m -th cell's ECM
R_S^m	Resistance of the series switch in Fig. 2
R_{S+}^m	Resistance of the parallel switch connecting the positive battery terminals in Fig. 2
R_{S-}^m	Resistance of the parallel switch connecting the negative battery terminals in Fig. 2
I_G^m	Current of the m -th battery cell, positive for charging
I_S^m	Current of the m -th parallel switch connecting the positive battery terminals
I_T	Current of the battery system, positive for charging
x	Battery state vector
SoC	State of charge
z_m	State of charge of the m -th battery cell
Q_m	Amount of charge of the m -th battery cell
$kV_S(z_m)$	The slope of the OCV-SoC curve at the cell SoC z_m
I_C	Battery cell current vector
A_C, B_C	System matrices corresponding to the system input I_C
KVL	Kirchhoff's voltage law
KCL	Kirchhoff's current law
E, F, G	Matrices relating the battery state and current vectors to the battery system current
A, B	System matrices corresponding to the system input I_T
I_S	Switch current vector
T_{SC}	The Transition matrix from the cell current vector I_C to the switch current vector I_S
C_S, D_S	Output matrices corresponding to the output I_S
R_C	The identical internal resistance for all battery cells
R_n	The identical resistance of the resistor in the n -th RC pair for all battery cells
C_n	The identical capacitance of the capacitor in the n -th RC pair for all battery cells
τ_n	Time constant of the n -th RC pair for all battery cells
R_S	The identical resistance for all switches
I_{Szi}	The switch current given zero system current, i.e., $I_T = 0$
I_{Szs}	The switch current given zero system states, i.e., $x = 0$
D_S^m	The m -th entry in the output vector D_S
I_{Tub}^m	Upper bound of the system current magnitude $ I_T $
$ I_{Szs}^{max} $	The maximum switch current magnitude given zero system states, i.e., $x = 0$
t_p	The time instant at which the system is reconfigured to the parallel operation
R_e^m	The equivalent resistance for the network composed of the first m parallel-connected cells
V_e^m	The equivalent voltage for the network composed of the first m parallel-connected cells
$\{\alpha_k\}$	A sequence derived for calculating the equivalent resistance R_e^m and the equivalent voltage V_e^m
ρ	The ratio between the switch resistance and the battery internal resistance
ϵ_c	Convergence tolerance for the sequence $\{\alpha_k\}$
$k_c(\rho, \epsilon_c)$	The smallest number of cells at which the sequence $\{\alpha_k\}$ converges given a convergence tolerance ϵ_c
V_{OC}^{ran}	Range of all M battery OCVs
V_{OC}^{lim}	Upper limit of all M battery OCVs' range
m^*	The battery cell index at which the equality can be achieved in Equation (26)
$S_\alpha^m(\rho, M)$	The sum of α_m and α_{M-m} given the resistance ratio ρ and the number of battery cells M
m^\dagger	The m at which $S_\alpha^m(\rho, M)$ is minimized given the resistance ratio ρ and the number of batteries M
$ I_{Szi}^{max} $	The maximum switch current magnitude given zero system current, i.e., $I_T = 0$

$ I_{Szi}^{ulim} $	The upper limit of the switch current magnitude given zero system current, i.e., $I_T = 0$
$ I_S^{max} $	The maximum switch current magnitude
$ I_S^{ulim} $	The upper limit of the switch current magnitude

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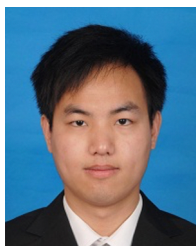


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